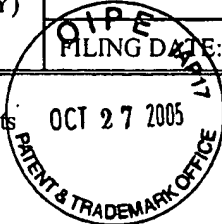


FORM PTO-1449 (MODIFIED)	ATTY. DOCKET NO. 681331-148	SERIAL NO. 10/779,475
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS INFORMATION DISCLOSURE STATEMENT (USE SEVERAL SHEETS IF NECESSARY)	APPLICANT: Alain CHAPUIS	
	FILING DATE: February 12, 2004	GROUP: 2838

Reference Designation

U.S. Patents

OCT 27 2005



Examiner Initial	Document Number	Date	Name	Class	Filing Date Subclass if appropriate
R	AA 4,616,142	10/07/86	Upadhyay et al.		
R	AB 4,654,769	03/31/87	Middlebrook		
R	AC 5,079,498	01/07/92	Cleasby et al.		
R	AD 5,229,699	07/20/93	Chu et al.		
R	AE 5,349,523	09/20/94	Inou et al.		
R	AF 5,532,577	07/02/96	Doluca		
R	AG 5,627,460	05/06/97	Bazinet et al.		
R	AH 5,727,208	03/10/98	Brown		
R	AI 5,752,047	05/12/98	Darty et al.		
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R	AL 5,892,933	04/06/99	Voltz		
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R	AY 6,208,127	03/27/01	Doluca		
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R	BE 2002/0105227	08/08/02	Nerone et al.		
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R	BH 2004/0123164	06/24/04	Chapuis et al.		
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R	BN 2004/0246754	12/09/04	Chapuis		

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<i>f</i>	BO EP0660487	06/28/95	Europe	<i>I</i>	
<i>f</i>	BP EP0875994	11/04/98	Europe	<i>I</i>	
<i>f</i>	BQ EP0997825	05/03/00	Europe	<i>I</i>	
<i>f</i>	BR WO02/31951	04/18/02	PCT	<i>I</i>	
<i>f</i>	BS WO02/50690	06/27/02	PCT	<i>I</i>	
<i>f</i>	BT WO93/19415	09/30/93	Great Britain	<i>I</i>	
<i>f</i>	BU SU1814177	05-1993	Russia	<i>I</i>	

OTHER ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

<i>f</i>	BV "Quantization Resolution and Limit Cycling in Digitally Controlled PWM Converters" by Angel V. Peterchev and A.V. Sanders	Electrical Engineering and Computer Science; UC Berkley; Power Electronics Specialists Conference, 2001.PESC, Vol. 2, 17-21 June 2001, pages 465- 471; XP002274573
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EXAMINER

fj. Law

DATE CONSIDERED

11/18/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.